

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peramachanahalli S Ramkumar et al.

Title: COMBINED EMULATION AND SIMULATION DEBUGGING TECHNIQUES

Docket No.: 884.A45US1

Serial No.: 10/676,141

Filed: September 30, 2003

Due Date: July 17, 2007

Examiner: Chrystine Pham

Group Art Unit: 2192

MS AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

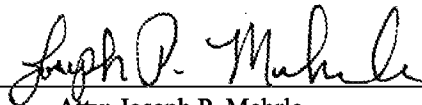
X Amendment and Response Under 37 CFR 1.116 (8 pgs.).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

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(GENERAL)